



Docket No.: 064965-0168 (G0971)

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qi XIANG

RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE

Application No.: 10/620,605

Customer No.: 20277

Filed: July 17, 2003

Confirmation No.: 9283

Group Art Unit: 2811

Examiner: Sara W. Crane

Title: CMOS WITH STRAINED SILICON CHANNEL NMOS AND SILICON
GERMANIUM CHANNEL PMOS

AMENDMENT UNDER 37 C.F.R. § 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

These amendments and remarks are filed in response to the final Office Action dated November 16, 2005, pursuant to 37 C.F.R. § 1.116. A petition for a one-month extension of time is being filed concurrently with this paper.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.